



LC 1209

60V Input / 5V,3.3V Output Linear Regulator

DESCRIPTION

LC1209 is a three-terminal positive regulator with an output voltage of 5.0V and output current up to 100mA. The device features a typical output tolerance of $\pm 3\%$. And its input voltage can stand a voltage as high as 60V.

LC1209 includes high accuracy voltage reference, error amplifier, TSD circuit and output driver module.

LC1209 offers thermal shut down functions to assure the stability of chip and power system.

LC1209 is available in SOT89-3, TO-92 and TO-220 power packages.

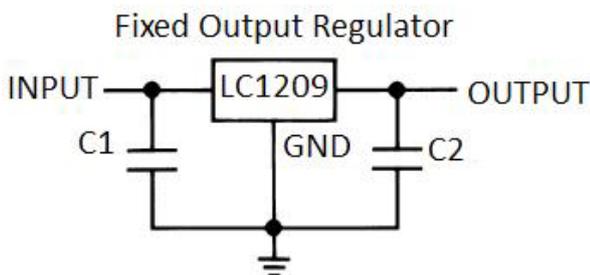
FEATURES

- Maximum output current up to 100mA
- Output voltage tolerances of $\pm 3\%$ over the temperature range
- Internal thermal over-temperature protection
- High input voltage (up to 60V)
- Low Power Consumption: 80uA (Typ.)
- Available in plastic TO-92 and plastic TO-220 packages
- No external components

APPLICATIONS

- Battery Powered equipment
- Communication equipment
- Audio/Video equipment

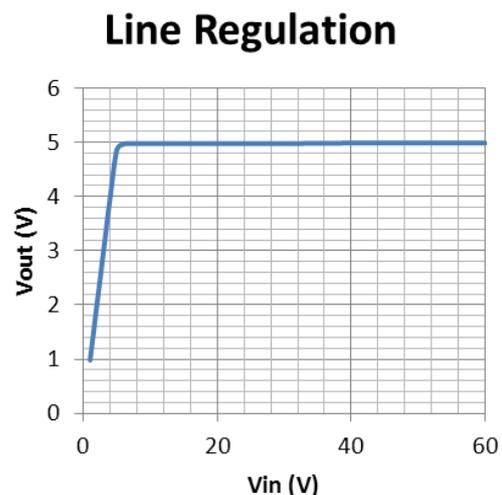
TYPICAL APPLICATION



Application circuit of LC1209

NOTE: Input capacitor ($C1=0.33\mu F$) and Output capacitor ($C2=0.1\mu F$) are recommended in all application circuit. Tantalum capacitor is recommended.

ELECTRICAL CHARACTERISTICS

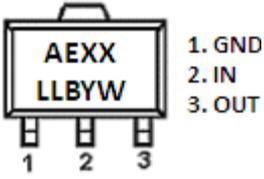
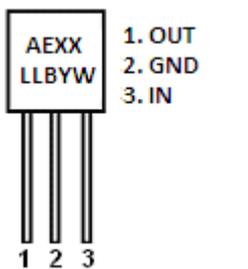
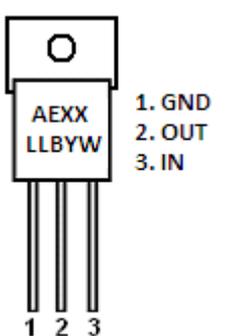


ORDERING INFORMATION

LC1209 [1](#) [2](#) [3](#) [4](#) [5](#)

Code	Description
1	Temperature&RoHS: C:-40~85°C ,Pb Free RoHS Std.
2	Package type: C3:SOT-89-3 H:TO-92 N: TO220
3	Packing type: TR:Tape&Reel (Standard) BG:Bag (TO-92)
4	Output voltage: e.g. 11=1.1V 15=1.5V 55=5.5V
5	Voltage accuracy: 2=±2% Blank(default)=±3%

PIN CONFIGURATION

Marking Explanation		LC1209CC3TR 4 4 SOT89-3
AEXX LLBYW	N: Product Code	
	XX: Output Voltage	
	LL: LOT NO.	
	B: FAB Code	
	YW: Date Code	
Marking Explanation		LC1209CC3BTR 4 4 SOT89-3
AEXX LLBYW	N: Product Code	
	XX: Output Voltage	
	LL: LOT NO.	
	B: FAB Code	
	YW: Date Code	
Marking Explanation		LC1209CHBG 4 4 TO92
AEXX LLBYW	N: Product Code	
	XX: Output Voltage	
	LL: LOT NO.	
	B: FAB Code	
	YW: Date Code	
Marking Explanation		LC1209CNBG 4 4 TO220
AEXX LLBYW	N: Product Code	
	XX: Output Voltage	
	LL: LOT NO.	
	B: FAB Code	
	YW: Date Code	

RECOMMENDED WORK CONDITIONS

Parameter	Value
Input Voltage Range	Max 60V
Operating Junction Temperature(Tj)	-20°C –85°C

ABSOLUTE MAXIMUM RATING

Parameter		Value
Max Input Voltage		60V
Max Output Current		100mA
Operating Junction Temperature(Tj)		150°C
Ambient Temperature(Ta)		-40°C –85°C
Power Dissipation	TO-92	0.5 W
	TO-220	1 W
	SOT89-3	0.5W
Storage Temperature(Ts)		-40°C -150°C
Lead Temperature & Time		260°C, 10s

Note:

- Exceed these limits may cause damage to the device.
- Exposure to absolute maximum rating conditions may affect device reliability.

ELECTRICAL CHARACTERISTICS

(Test Conditions: $C_{in}=0.33\mu F, C_{out}=0.1\mu F, T_A=25^\circ C$, Unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Vin	Input Voltage				60	V
Vout	Output Voltage	1mA ≤ Iout ≤ 40mA 7V ≤ Vin ≤ 30V	4.85	5.0	5.15	V
		1mA ≤ Iout ≤ 40mA 5.3V ≤ Vin ≤ 30V	3.2	3.3	3.4	V
ΔVout	Line Regulation	7V ≤ Vin ≤ 30V	-	-	200	mV
ΔVout	Load Regulation	1mA ≤ Iout ≤ 100mA	-	-	150	mV
Iout(Max.)	Maximum Output	Vin - Vout = 1.5V	100			mA
Iq	Quiescent Current	Vin - Vout = 1.25V	-	0.08	0.15	mA
ΔV/ΔT	Temperature coefficient	Vin = 6.5V, 25°C ≤ Temp ≤ 85°C			±100	ppm
TSD	Over Temperature	Vin = 6.5V, Iout = 1mA	150			°C
θJC	Thermal Resistor	TO-92		10		°C / W
		TO-220		4.5		
		SOT89-3		20		

Note1: All test are conducted under ambient temperature 25°C and within a short period of time 20ms

BLOCK DIAGRAM

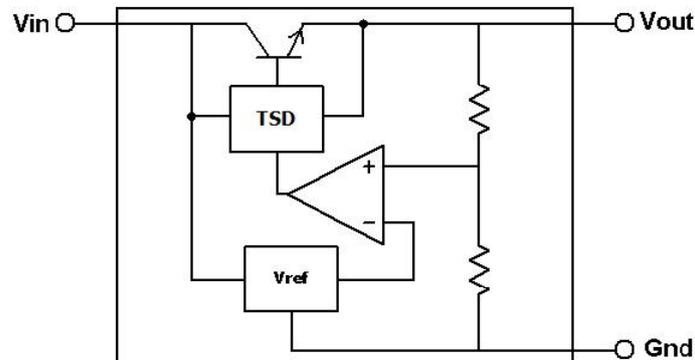


Fig.1 Block Diagram

EXPLANATION and THERMAL CONSIDERATION

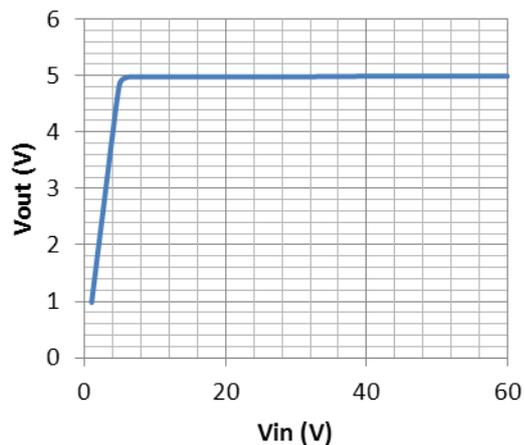
LC1209 is a series of low dropout voltage and low power consumption regulator. Its application circuit is very simple, which only needs two outside capacitors.

We have to take heat dissipation into great consideration when voltage of input is high. Because in such cases, the power dissipation consumed by LC1209 is very large. LC1209 uses SOT-89-3 package type and its thermal resistance is about $20^{\circ}\text{C}/\text{W}$. And the copper area of application board can affect the total thermal resistance. If copper area is $5\text{cm} \times 5\text{cm}$ (two sides), the resistance is about $30^{\circ}\text{C}/\text{W}$. So the total thermal resistance is about $20^{\circ}\text{C}/\text{W} + 30^{\circ}\text{C}/\text{W}$. We can decrease total thermal resistance by increasing copper area in application board. When there is no good heat dissipation copper are in PCB, the total thermal resistance will be as high as $120^{\circ}\text{C}/\text{W}$, then the power dissipation of LC1209 could allow on itself is less than 1W . And furthermore, LC1209 will work at junction temperature higher than 125°C under such condition and no lifetime is guaranteed.

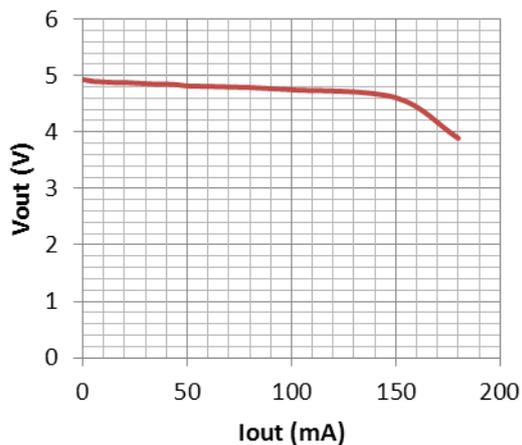
TYPICAL PERFORMANCE CHARACTERISTICS

($T=25^{\circ}\text{C}$ unless specified.)

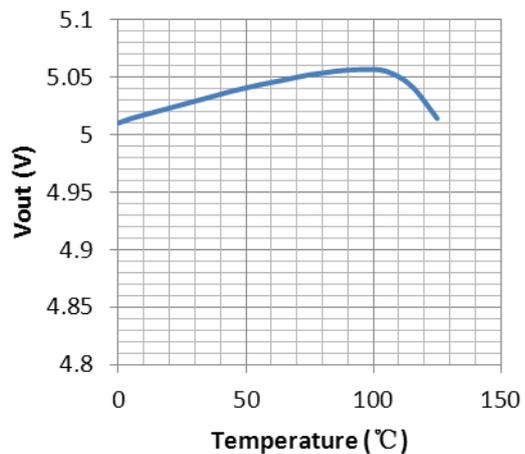
Line Regulation



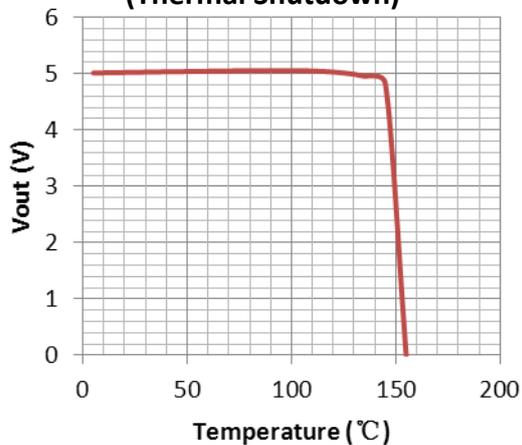
Load Regulation



Temperature Coefficient



TSD (Thermal Shutdown)



PACKAGE OUTLINE

Package	TO-92	Device per Box	1000	Unit	mm
Package specification:					
<p> EJECTION MARK $\varnothing 1.6$ MAX $\nabla 0.38$ MAX </p> <p>SEATING PLANE</p> <p>2.29 MAX (UNCONTROLLED LEAD DIA)</p> <p>0.55 TYP 0.40</p> <p>5.2 4.7</p> <p>4.19 3.65</p> <p>5.2 4.9</p> <p>14.2 12.7</p> <p>1.27± 0.05</p> <p>2.54± 0.1</p> <p>0.50 0.36</p> <p>2.27 2.10</p> <p>DIMENSIONS ARE IN MILLIMETERS</p>					

Package	SOT-89-3	Devices per reel	1000	Unit	mm
Package specification:					
<p> 4.5 ± 0.1 1.6 ± 0.2 0.4 2.5 ± 0.1 4.25 MAX. 0.8 MIN. $\varnothing 1.0$ 1.5 ± 0.1 0.4 ± 0.1 0.4 ± 0.1 0.42 ± 0.2 0.47 ± 0.1 1.5 ± 0.1 1.5 ± 0.1 0.42 ± 0.2 </p>					

PACKAGE OUTLINE (Continued)

Package	TO-220	Devices per reel		Unit	mm
Package specification:					
<p>The technical drawing illustrates the TO-220 package with the following dimensions:</p> <ul style="list-style-type: none"> Top View: <ul style="list-style-type: none"> Overall width: 9.90 ± 0.20 mm Distance from center to lead edge: 1.30 ± 0.10 mm Lead diameter: $\phi 3.60 \pm 0.10$ mm Distance from center to mounting hole: 2.80 ± 0.10 mm Mounting hole diameter: (8.70) mm Front View: <ul style="list-style-type: none"> Lead length: 13.08 ± 0.20 mm Lead diameter: 1.27 ± 0.10 mm Lead tip diameter: 1.52 ± 0.10 mm Lead tip angle: 45° Distance from lead tip to mounting hole: 10.08 ± 0.30 mm Mounting hole diameter: 0.80 ± 0.10 mm Lead thickness: 2.54 TYP [2.54 ± 0.20] mm Distance from lead tip to package body: 1.00 mm Distance from lead tip to package body: (1.46) mm Distance from lead tip to package body: (3.00) mm Distance from lead tip to package body: (3.70) mm Distance from lead tip to package body: 15.90 ± 0.20 mm Distance from lead tip to package body: 18.95 MAX. mm Side View: <ul style="list-style-type: none"> Package body width: 4.50 ± 0.20 mm Distance from lead tip to package body: 1.30 $\begin{matrix} +0.10 \\ -0.05 \end{matrix}$ mm Lead diameter: 0.50 $\begin{matrix} +0.10 \\ -0.05 \end{matrix}$ mm Lead thickness: 2.40 ± 0.20 mm Bottom View: <ul style="list-style-type: none"> Package body width: 10.00 ± 0.20 mm 					